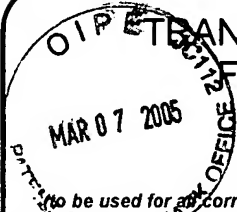
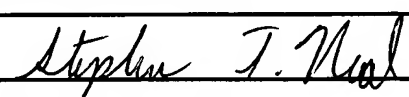


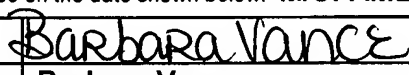
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	Application Number	10/625,240	
	Filing Date	July 22, 2003	
	First Named Inventor	Opher D. KAHN et al.	
	Art Unit	2183	
	Examiner Name	TBA	
Total Number of Pages in this Submission	4	Attorney Docket Number	Intel 2207/670602

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) ____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): 1. PTO-1449 2. Postcard
<div style="border: 1px solid black; padding: 5px;">Remarks</div>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm	Kenyon & Kenyon	
Signature		
Printed Name	Stephen T. Neal	
Date	March 1, 2005	Reg. No. 47,815

CERTIFICATE OF TRANSMISSION/MAILING		
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. M/S: AMENDMENT		
Signature		
Typed or printed name	Barbara Vance	Date March 1, 2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Patent



Attorney Docket No.: Intel 2207/670602
Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Opher D. KAHN et al.
SERIAL NO. : 10/625,240
FILED : July 22, 2003
FOR : METHOD AND APPARATUS TO SUPPORT
AN EXPANDED REGISTER SET
GROUP ART UNIT : 2183
EXAMINER : TBA

M/S: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Date: <u>March 1, 2005</u>	Signature <u>Barbara Vance</u>
Barbara Vance	

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Submitted herewith is an Information Disclosure Statement under 37 CFR §1.97(b)(3) with ten (10) reference(s) listed on the attached PTO-1449 (modified) form. A copy of the cited U.S. Patent Document references is not enclosed since the present application was filed after June 30, 2003. (See OG Dated: August 5, 2003). One copy of each patent literature reference is attached.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear

among the "References Cited" on any patent to issue therefrom.

If any fee is required, the Office is hereby authorized to charge any fees, or credit any overpayments under 37 CFR §1.17(p) to Kenyon & Kenyon, Deposit Account No. **11-0600**.

Respectfully submitted,

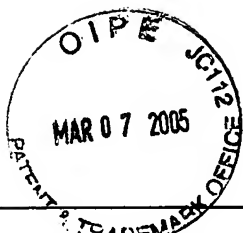
KENYON & KENYON

Dated: March 1, 2005

By: Stephen T. Neal
Stephen T. Neal
(Reg. No. 47,815)
Attorneys for Intel Corporation

KENYON & KENYON
333 West San Carlos St., Suite 600
San Jose, CA 95110

Telephone: (408) 975-7500
Facsimile: (408) 975-7501



Form PTO-1449 (modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT	Attorney Docket No. Intel 2207/670602	Serial No. 10/625,240
	Applicant Opher D. KAHN et al.	
	Filing Date July 22, 2003	Group Art Unit 2183

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	**5,446,912	August 29, 1995	Colwell et al.	395	800	December 29, 1993
	**6,014,739	January 11, 2000	Christie	712	228	October 27, 1997
	**6,157,996	December 5, 2000	Christie	712	218	November 13, 1997
	**6,230,259	May 8, 2001	Christie et al.	712	228	October 31, 1997

**A copy is not enclosed since the present application was filed after June 30, 2003. (See OG Dated: August 5, 2003)

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Pentium® Pro Family Developer's Manual; Volume 2: Programmer's Reference Manual , pps. 11-1 – 11.3, 1996.
	Intel Architecture Software Developer's Manual; Volume 2: Instruction Set Reference, pps. 2-1 – 2-6, 1997
	Intel Architecture Software Developer's Manual; Volume 1: Basic Architecture, pps. 2-1 – 2-13, 1999
	Intel Architecture Software Developer's Manual; Volume 2: Instruction Set Reference, pps. B-19 – B-34, 1999
	Chia-Lin Yang, Barton Sano, and Alvin S. Lebeck, "Exploiting Instruction Level Parallelism in Geometry Processing for Three Dimensional Graphics Applications", #0-8186-8609-X/98, pps. 14-24, 1998 IEEE

EXAMINER	DATE CONSIDERED
<p>EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	